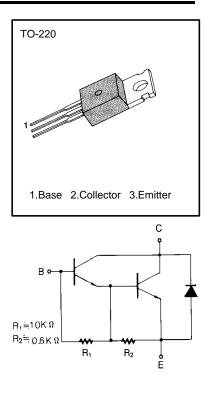
PNP EPITAXIAL TIP115/116/117 SILICON DARLINGTON TRANSISTOR

HIGH DC CURRENT GAIN MIN h_{FE} =1000 @ V_{CE} = -4V, I_{C} = -1A LOW COLLECTOR-EMITTER SATURATION VOLTAGE MONOLITHIC CONSTRUCTION WITH BUILT IN BASE-EMITTER SHUNT RESISTORS INDUSTRIAL USE

Complementary to TIP110/111/112

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit
Collector Base Voltage :TIP115	V _{CBO}	-60	V
: TIP116		-80	V
: TIP117		-100	V
Collector Emitter Voltage			
:TIP115	V _{CEO}	-60	V
:TIP116		-80	V
:TIP117		-100	V
Emitter-Base Voltage	V _{EBO}	-5	V
Collector Current (DC)	lc	-2	А
Collector Current (Pulse)	I _C	-4	Α
Base Current (DC)	I _B	-50	mA
Collector Dissipation (T _A =25°C)	Pc	2	W
Collector Dissipation (T _C =25°C)	Pc	50	W
Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-65 ~ 150	°C



ELECTRICAL CHARACTERISTICS (Tc =25°C)

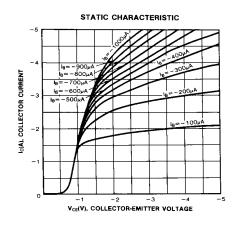
Characteristic	S	Symbol	Test Conditions	Min	Max	Unit
Collector Emitter Sustaining		_{EO} (sus)	$I_{\rm C} = -30 {\rm mA}, I_{\rm B} = 0$			
-	TIP115			-60		V
:	TIP116			-80		V
:	TIP117			-100		V
Collector Cutoff Current	TIP115 I _{CE}	0	$V_{CE} = -30V, I_{B} = 0$		-2	mA
: TIP116			$V_{CE} = -40V, I_{B} = 0$		-2	mA
:	TIP117		$V_{CE} = -50V, I_{B} = 0$		-2	mA
Collector Cutori Current	TIP115 I _{CB}	0	$V_{CB} = -60V, I_E = 0$		-1	mA
	TIP116		$V_{CB} = -80V, I_{E} = 0$		-1	mA
: TIP117	TIP117		$V_{CB} = -100V, I_E = 0$		-1	mA
Emitter Cutoff Current	IEB	80	$V_{BE} = -5V, I_{C} = 0$		-2	mA
DC Current Gain	h _F	E	$V_{CE} = -4V, I_{C} = -1A$	1000		
			$V_{CE} = -4V, I_{C} = -2A$	500		
Collector Emitter Saturation	Voltage V _c	∈(sat)	$I_{\rm C} = -2A, I_{\rm B} = -8mA$		-2.5	V
Base Emitter On Voltage	V _B	_E (on)	$V_{CE} = -4V, I_{C} = -2A$		-2.8	V
Output Capacitance	Co	в	$V_{CB} = -10V, I_E = 0, f = 0.1MHz$		200	pF

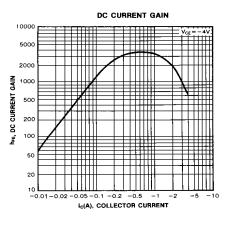


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Rev. B.1

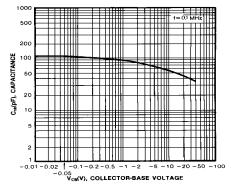
NPN EPITAXIAL TIP115/116/117 SILICON DARLINGTON TRANSISTOR





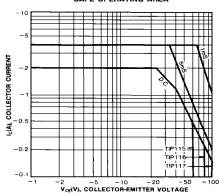
COLLECTOR-EMITTER SATURATION VOLTAGE BASE-EMITTER SATURATION VOLTAGE - 10 VOLTAGE -20 (V), SATURATION -10 - 5 -2 V_{CE}(sat) -TTTL -0.5 (sat), V^{BE} -0.2 -0.1 -0.1 -0.2 -0.5 _ 0 -5 -10 Ic(A), COLLECTOR CURRENT





POWER DERATING

SAFE OPERATING AREA



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